



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/586,218	07/14/2006	Erik J. Marinissen	NL04 0065 US1	2749
65913	7590	10/10/2008	EXAMINER	
NXP, B.V.			KERVEROS, JAMES C	
NXP INTELLECTUAL PROPERTY DEPARTMENT			ART UNIT	PAPER NUMBER
M/S41-SJ			2117	
1109 MCKAY DRIVE				
SAN JOSE, CA 95131				
NOTIFICATION DATE		DELIVERY MODE		
10/10/2008		ELECTRONIC		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

<b>Office Action Summary</b>	<b>Application No.</b> 10/586,218	<b>Applicant(s)</b> MARINISSEN ET AL.
	<b>Examiner</b> JAMES C. KERVEROS	<b>Art Unit</b> 2117

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If no period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 04 September 2008.

2a) This action is FINAL.      2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-25 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-25 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 04 September 2008 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_

4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_

5) Notice of Informal Patent Application

6) Other: \_\_\_\_\_

**DETAILED ACTION**

This is a FINAL Office Action in response to the Amendment filed 9/4/2008.

The present US Application 10/586218, filed 07/14/2006, is a national stage entry of PCT/IB05/50153 international Filing Date: 01/13/2005.

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), for the EPO Application No. 04100141.3, filed 01/19/2004, which papers have been placed of record in the file.

The drawings replacement sheets for Figs. 1-3 received on 9/4/2008 are acceptable.

Objection to the specification has been withdrawn in view of a new title suggested by the Examiner.

Objection to the Claims has been withdrawn in view of the amendment to the claims.

Claims 1-25 are presently under examination and pending.

***Response to Arguments***

Applicant's arguments with respect to the rejection of Claims 1-25 under 35 U.S.C. 102(e) as being anticipated by Corbin et al. (US Patent No. 7,103,814, have been considered but are moot in view of the new ground(s) of rejection, as set forth in the present Office Action, next.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-25 are rejected under 35 U.S.C. 102(e) as being anticipated by Park et al. (U.S. Patent No. 7,296,200), filed: November 24, 2004.

Regarding independent Claims 1, 13, Park discloses a System-on-a-Chip (SoC) based scan chain test access mechanism (TAM) that facilitates various dynamic tests by guaranteeing reconfiguration capability while minimizing the hardware overhead of the test access configuration of intellectual property cores embedded in a system-on-a-chip, comprising:

A test access mechanism (TAM) arranged to transport test stimulus and responses to and from an (IP CORE) under test, Figs. 1-4. In a prior art, Fig. 2, shows a Core Access Switch (CAS) switch N/P that links P lines from N test bus terminals to scan chain input terminals and links P core scan chain output terminals to the test buses again.

A global enable signal (ENABLE) provided as an input to a TAM associated with respect to claims a respective IP CORE, as shown in Fig. 4, for placing the IP CORE in a test mode. To allow the TAM to operate in Test mode, '1' is applied as the Enable signal, and '0' is applied as the Mode signal. In Test mode, tests are performed through the linkage configuration of the switch constructed by the instruction applied in Configuration mode, and tests, such as a scan, a BIST and a wrapper scan, are performed

A control circuit (core scan chain linkage switch) associated with a respective IP CORE provided between the global enable signal (ENABLE) and the associated IP CORE, where the linkage switch is linked to the scan chains of the embedded core, according to TAM Configuration mode, where the ENABLE and the MODE signals are "1" s. The Configuration mode determines the linkage configuration of the core scan chain linkage switch connected to an IP CORE, according to a scan link instruction and update registers.

Regarding Claims 2-12, 14-25, Park discloses when Enable signal is '0', the TAM operates in Bypass mode regardless of the Mode signal, and all the pins of the test bus terminals pass through the switch without passing the scan chain input/output terminals and requiring clocks. To allow the TAM to operate in Test mode, '1' is applied as the Enable signal, and '0' is applied as the Mode signal. In Test mode, tests are performed through the linkage configuration of the switch constructed by the instruction applied in Configuration mode, and tests, such as a scan, a BIST and a wrapper scan, are performed.

***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques H. Louis-Jacques can be reached on (571) 272-4150. The fax

phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JAMES C KERVEROS/  
Primary Examiner, Art Unit 2117

Date: 9 October 2008

Office Action: Final Rejection

U.S. Patent & Trademark Office  
Alexandria, VA 22314.  
Tel: (571) 272-3824, Fax: (571) 273-3824  
Email: [james.kerveros@uspto.gov](mailto:james.kerveros@uspto.gov)